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• NOT ADMITTED IN NEW YORK
• REGISTERED PATENT AGENT

BY EXPRESS MAIL ON
August 31, 2001
(EK 825 217 053 US)

Box PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
Washington, DC 20231

RECORD OF TRANSMITTAL

INVENTORS: (1) Jun OSANAI
(2) Hisashi HASEGAWA
(3) Sumio KOIWA
(4) Kazutoshi ISHII

TITLE: COMPLEMENTARY MOS SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME

DOCKET NO: S004-4393

S I R:

Kindly find enclosed herewith for filing the documents
checked below. Any fees or charges not covered by the
accompanying check should be charged to our Deposit Account
No. 01-0268.

APPLICABLE ITEMS CHECKED:

XX PATENT APPLICATION (pages 1-168 of specification, 73
claims (pages) 1-71 and abstract of the
disclosure (page 1))
 DESIGN APPLICATION (specification, claim, declaration and
power of attorney and photograph(s))
XX FORMAL DRAWINGS (55 sheets) (8.5 x 11)
 INFORMAL DRAWINGS (sheets)
 RECORDATION FORM COVER SHEET and ASSIGNMENT
(to be recorded and returned to the undersigned)

XX PRIORITY CLAIM - applicants claim the benefit of priority of the following applications:

Japanese Patent Application No. 2000-265495 filed Sept. 1, 2000;
Japanese Patent Application No. 2000-299549 filed Sept. 29, 2000;
Japanese Patent Application No. 2000-331080 filed Oct. 30, 2000;
Japanese Patent Application No. 2001-035800 filed Feb. 13, 2001;
Japanese Patent Application No. 2001-044068 filed Feb. 20, 2001;
Japanese Patent Application No. 2001-113049 filed Apr. 11, 2001;
Japanese Patent Application No. 2001-118539 filed Apr. 17, 2001;
Japanese Patent Application No. 2001-188051 filed Jun. 21, 2001;
Japanese Patent Application No. 2001-204758 filed Jul. 5, 2001;
Japanese Patent Application No. 2001-241293 filed Aug. 8, 2001;
Japanese Patent Application No. 2001-241294 filed Aug. 8, 2001;
Japanese Patent Application No. 2001-241297 filed Aug. 8, 2001;
Japanese Patent Application No. 2001-241298 filed Aug. 8, 2001;
Japanese Patent Application No. 2001-241299 filed Aug. 8, 2001;
Japanese Patent Application No. 2001-241829 filed Aug. 9, 2001;
Japanese Patent Application No. 2001-243944 filed Aug. 10, 2001;

XX PRIORITY DOCUMENT(s)

 Enclosed

XX Not Enclosed

 VERIFIED STATEMENT TO ESTABLISH SMALL ENTITY STATUS

XX EXPRESS MAIL CERTIFICATION

 OTHER -

XX FEE

A check is enclosed for the following official fees:

	Large Entity	Small Entity
<u>XX</u> Basic Filing Fee	\$ 710.00	\$
<u>4</u> Independent Claims in Excess of 3	\$ 320.00	\$
<u>53</u> Total Claims in Excess of 20	\$ 954.00	\$
<u> </u> Multiple Claim Fee	\$	\$

____ Assignment Recordal Fee \$ \$
TOTAL \$1984.00 \$

Respectfully submitted,

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By: 

Bruce L. Adams
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Re: New patent application of Jun OSANAI et al.

for COMPLEMENTARY MOS SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME

comprising transmittal letter, title, specification (pgs.1-168),
73 claims (pgs.1-71), abstract of the disclosure (pg.1), 55
sheets of formal drawings containing thereon Figs. 1-91, express
mail certification and check in the amount \$1984.00

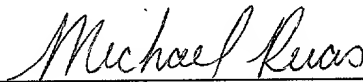
Attorney's Docket No: S004-4393

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